

Fig. 1A

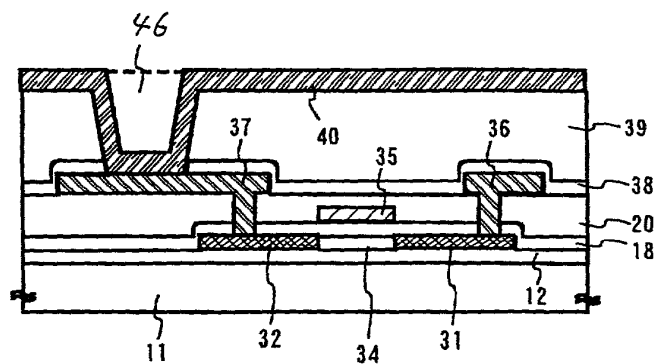


Fig. 1B

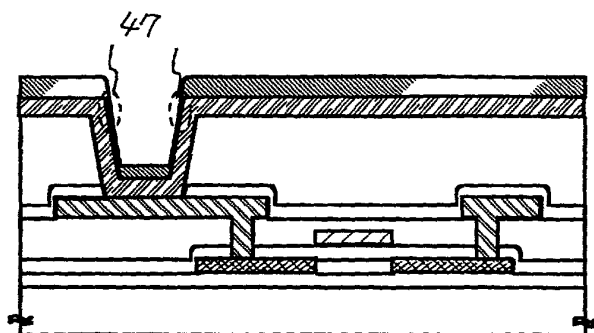
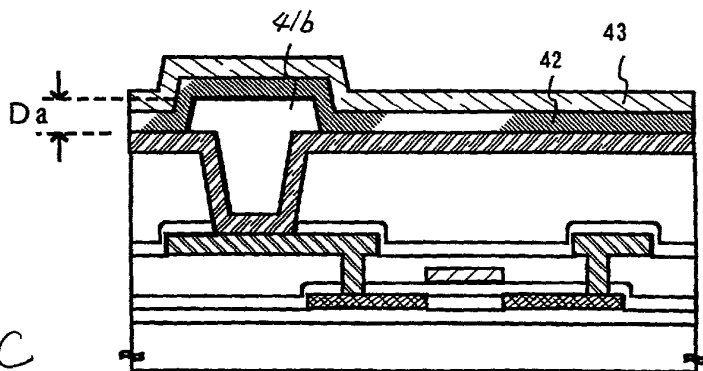


Fig. 1C



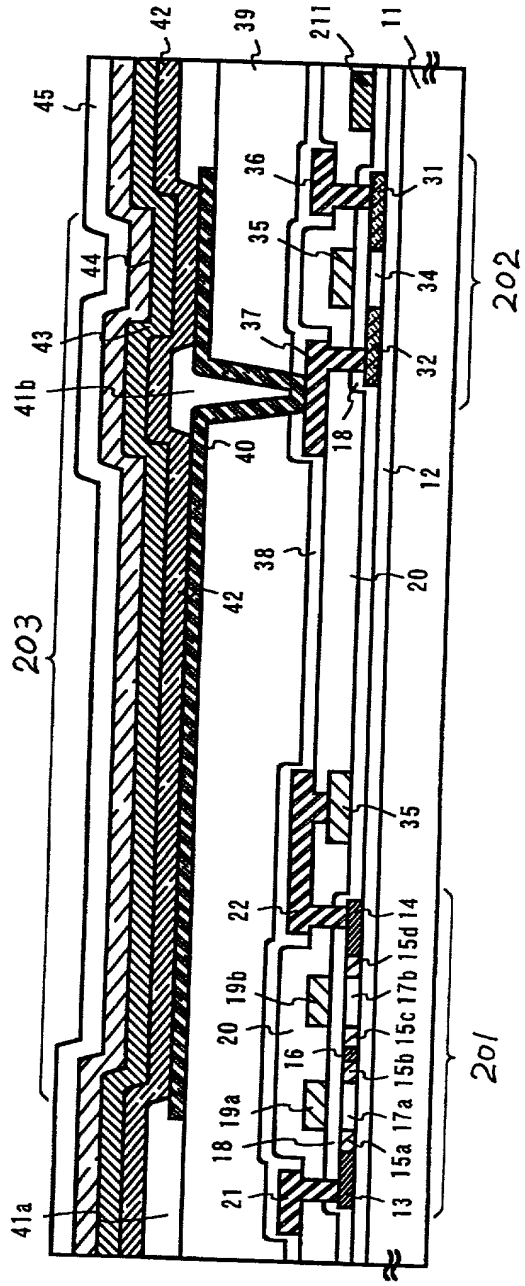


Fig. 2

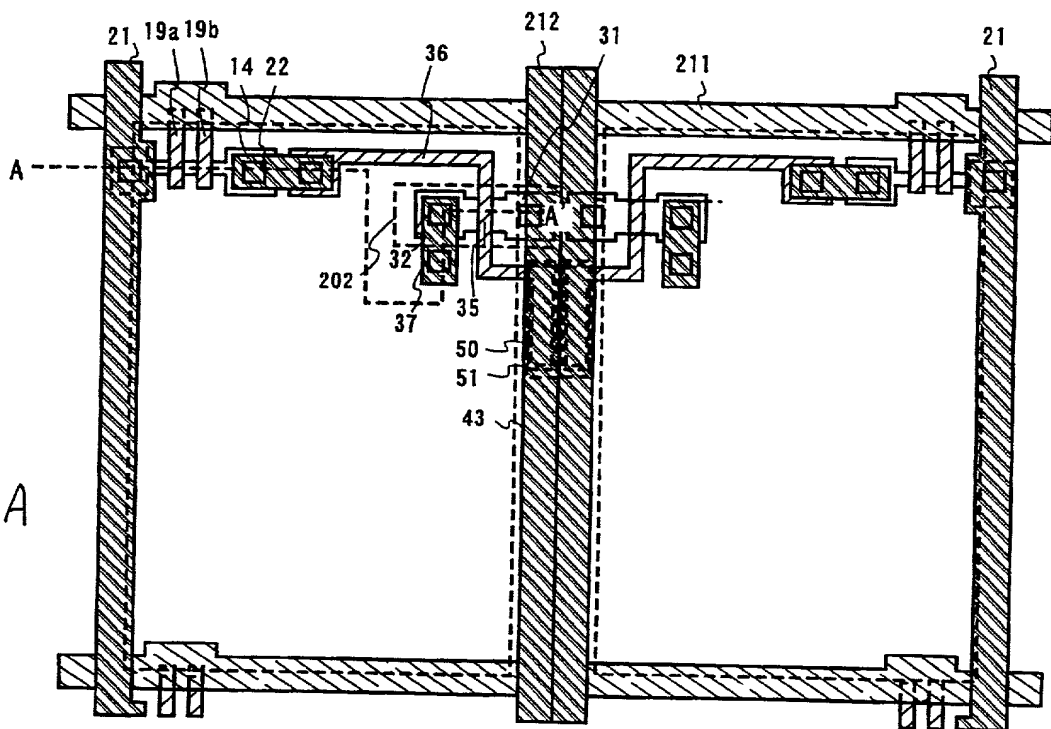


Fig. 3A

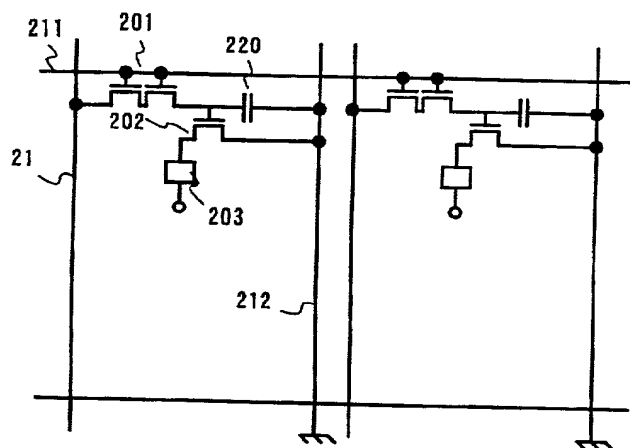


Fig. 3B

FIG. 4A

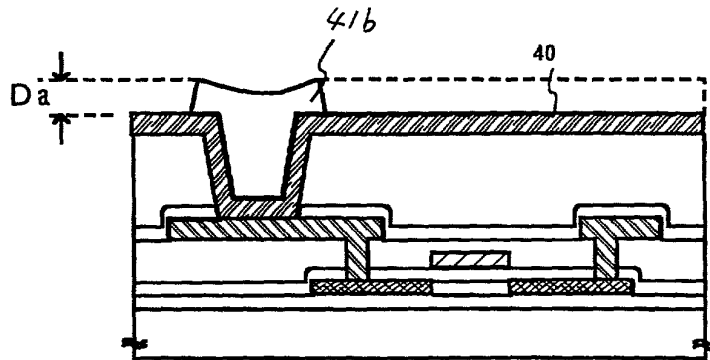


Fig. 4A

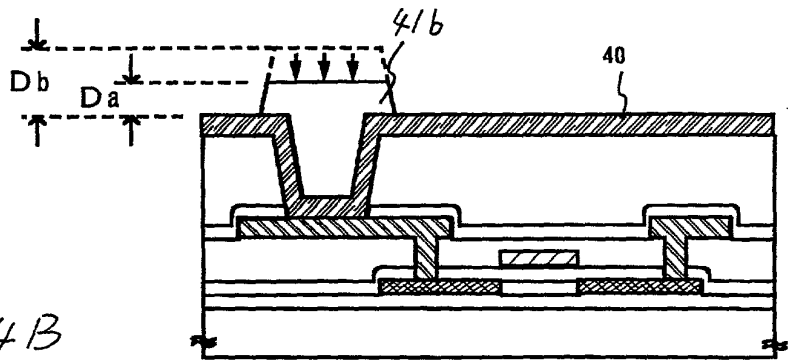


Fig. 4B

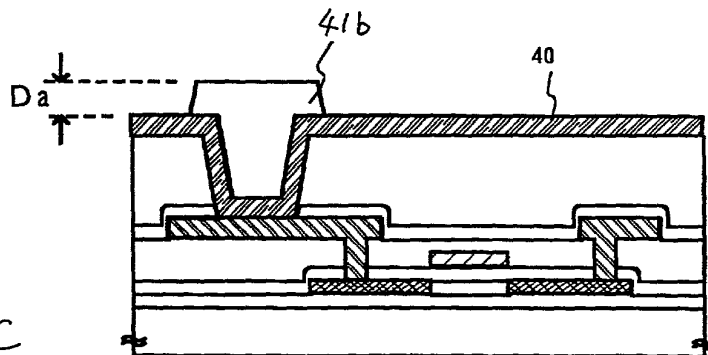


Fig. 4C

Fig. 5A

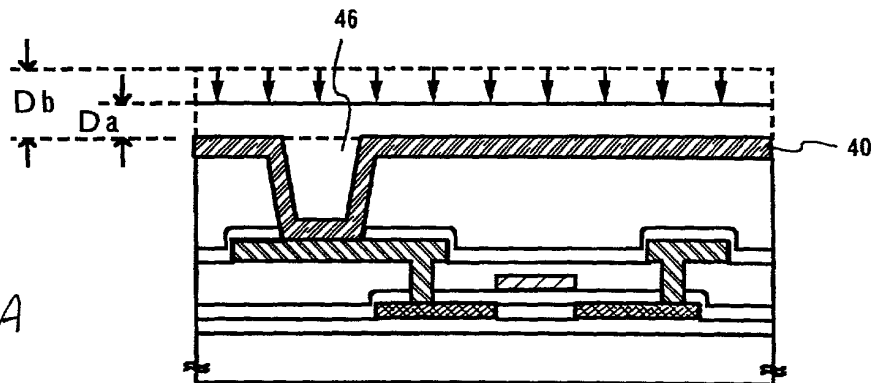


Fig. 5B

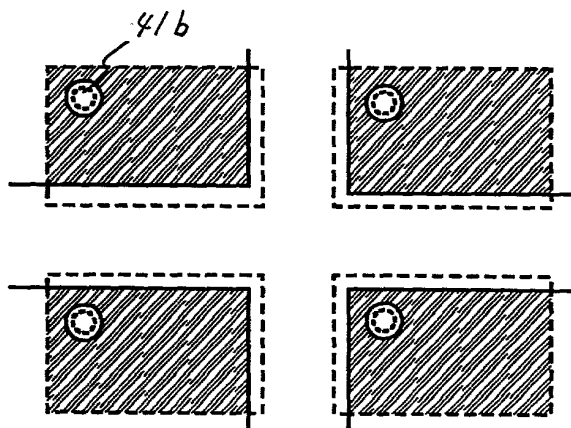
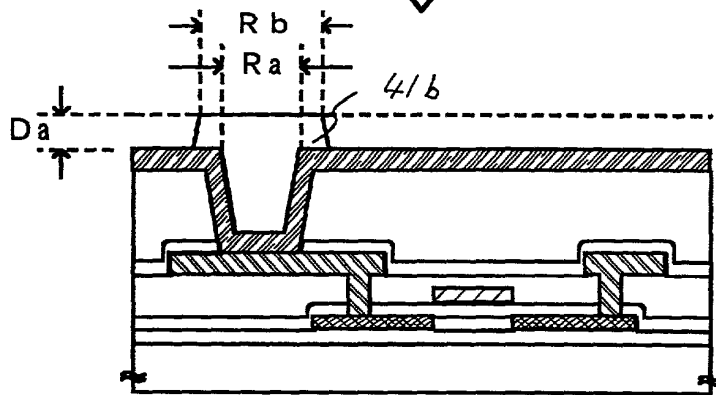


Fig. 5C

Fig. 6A



Fig. 6B

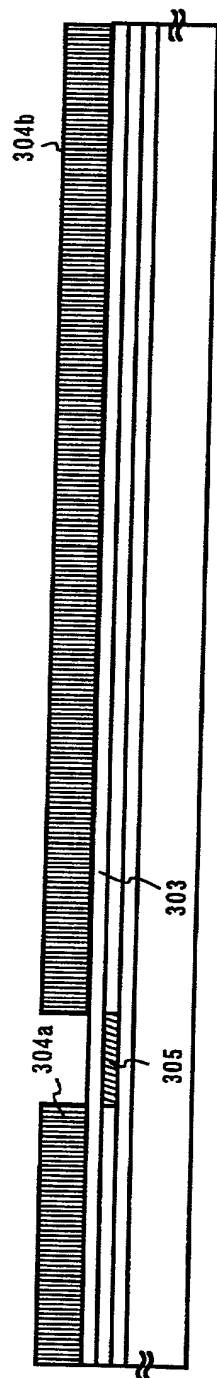


Fig. 6C

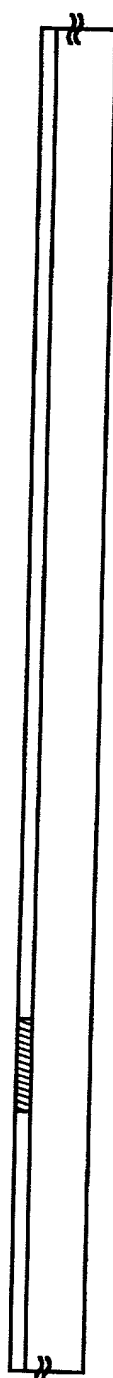


Fig. 6D

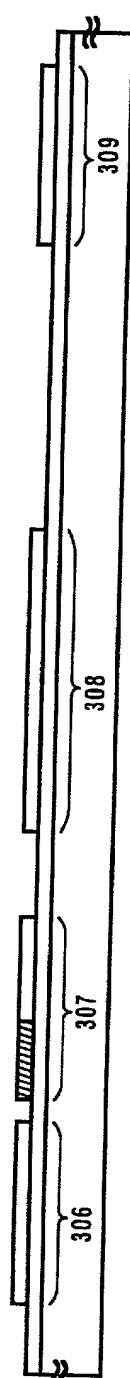
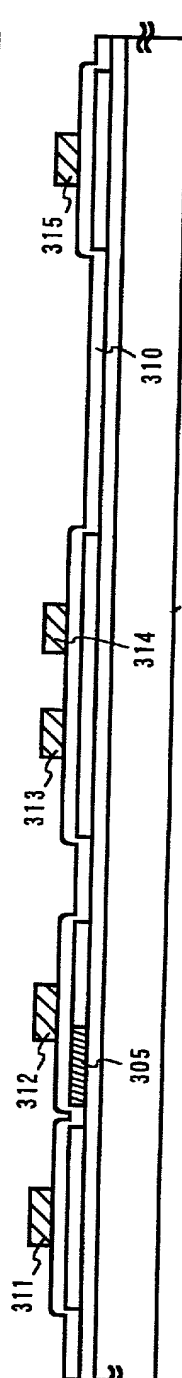


Fig. 6E



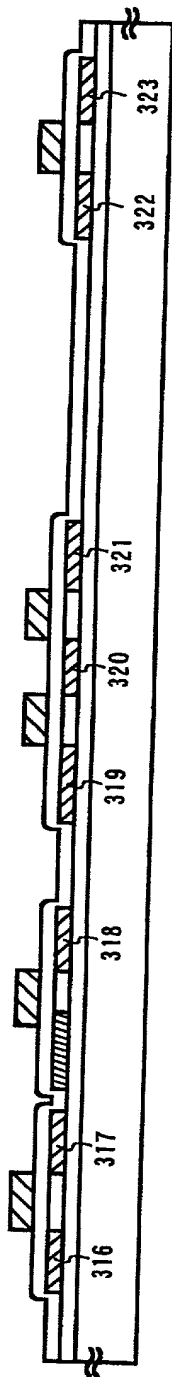


Fig. 7A

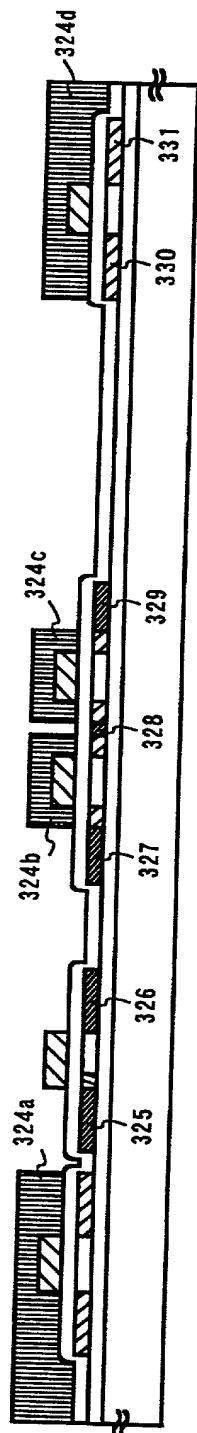


Fig. 7B

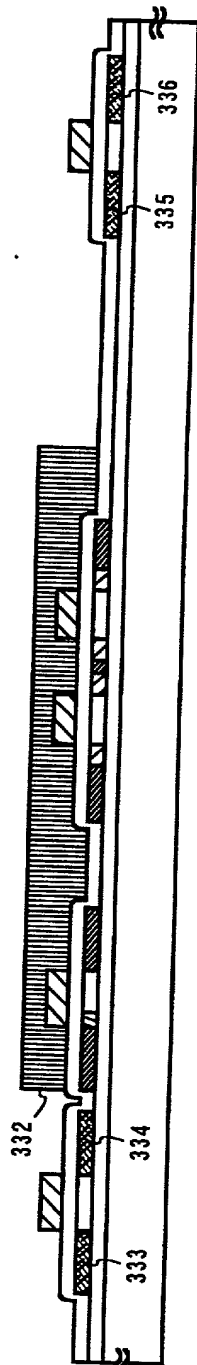


Fig. 7C

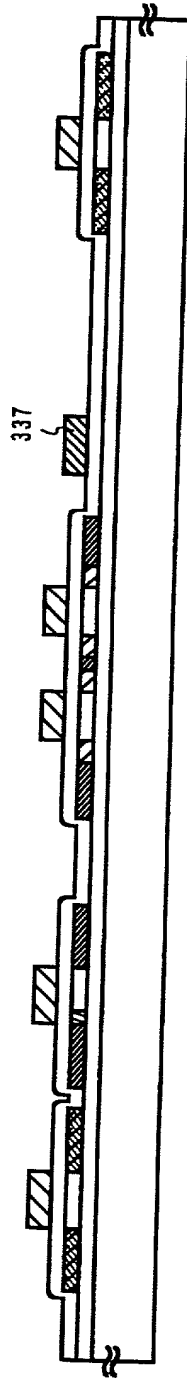


Fig. 7D

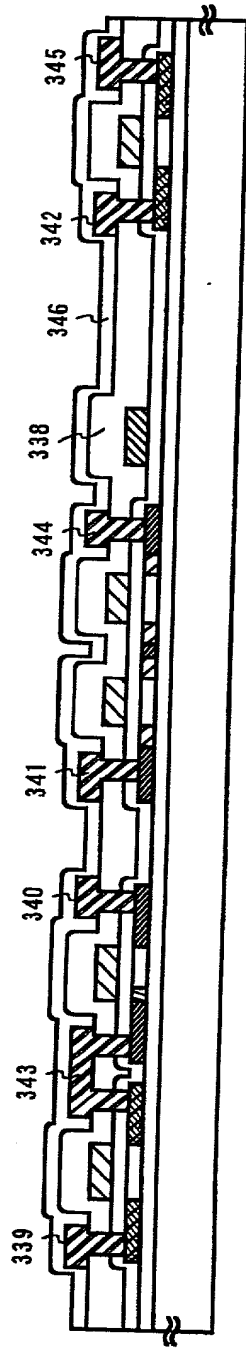


Fig. 8A

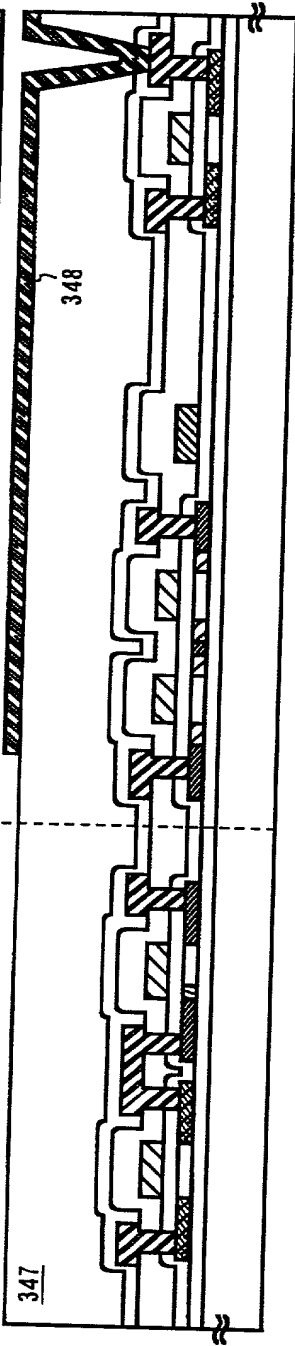


Fig. 8B

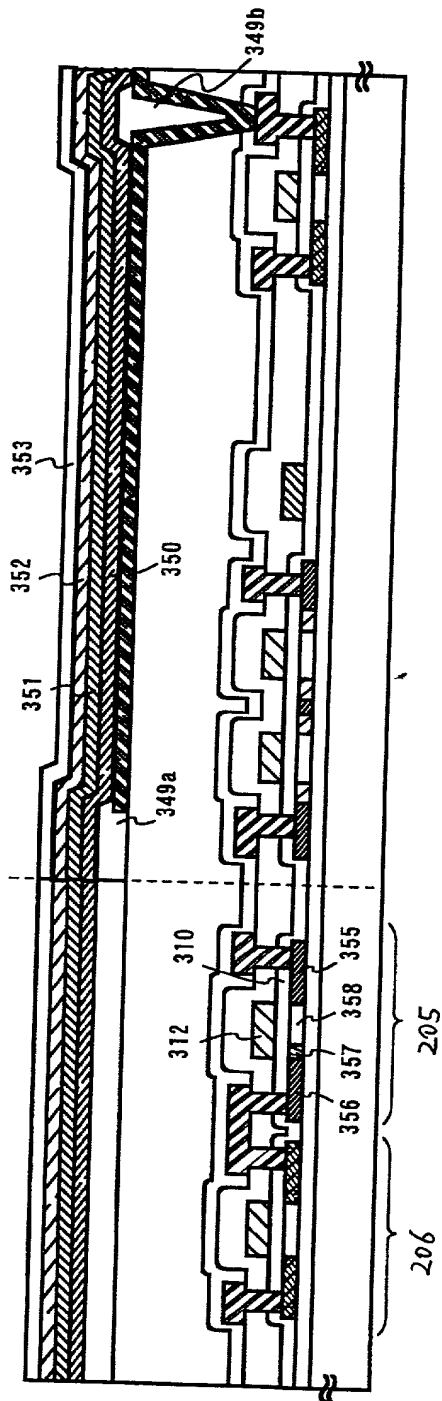


Fig. 8C



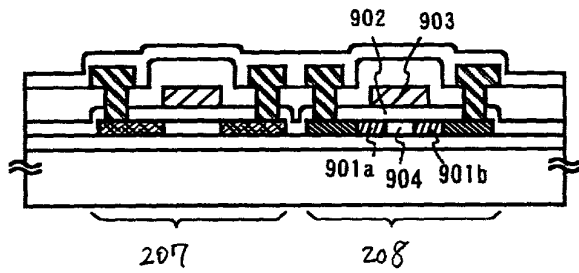


Fig. 9

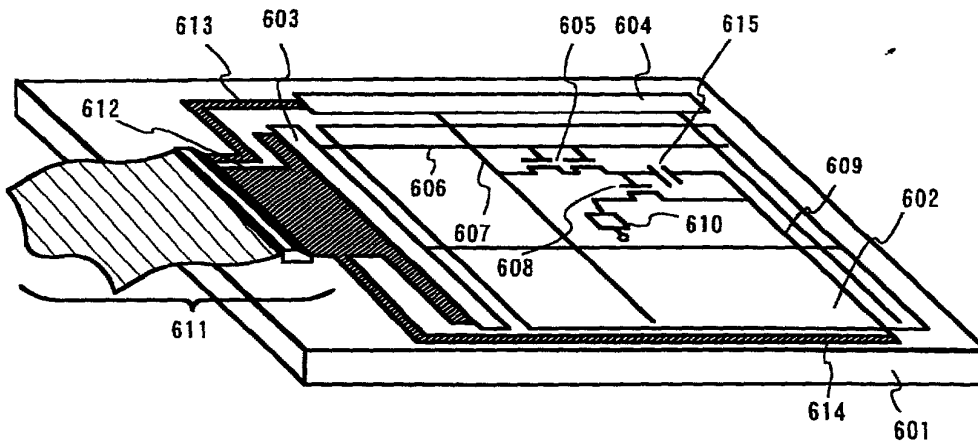


Fig. 10

801:source driver circuit

802:shift register

803:level shifter

804:buffer

805:sampling circuit

806:pixel portion

807: gate driver circuit (A)

808: shift register

809: level shifter

810: buffer

811: gate driver circuit (B)

Fig. 11

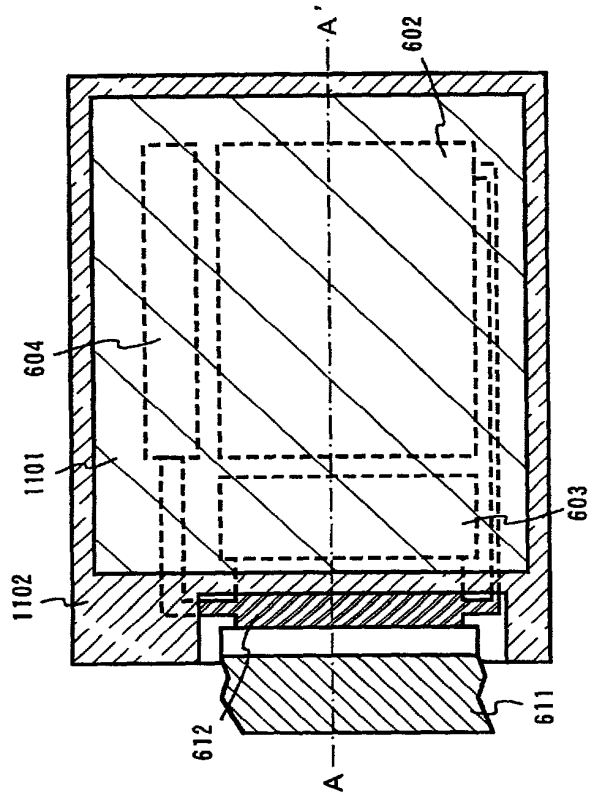


Fig. 12A

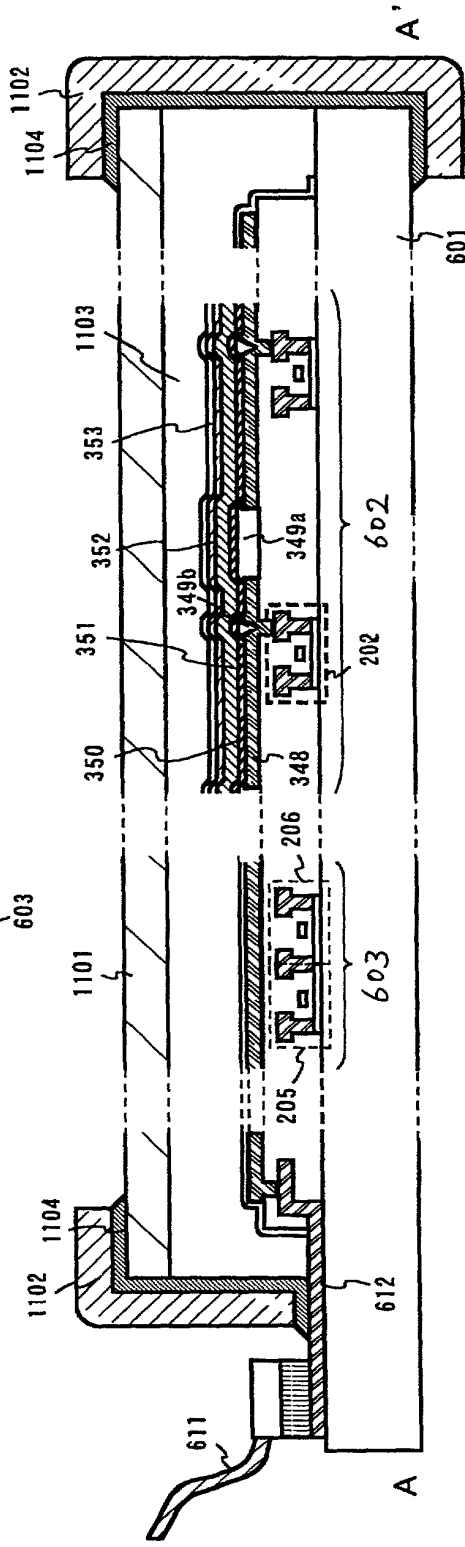


Fig. 12B

Fig. 13A

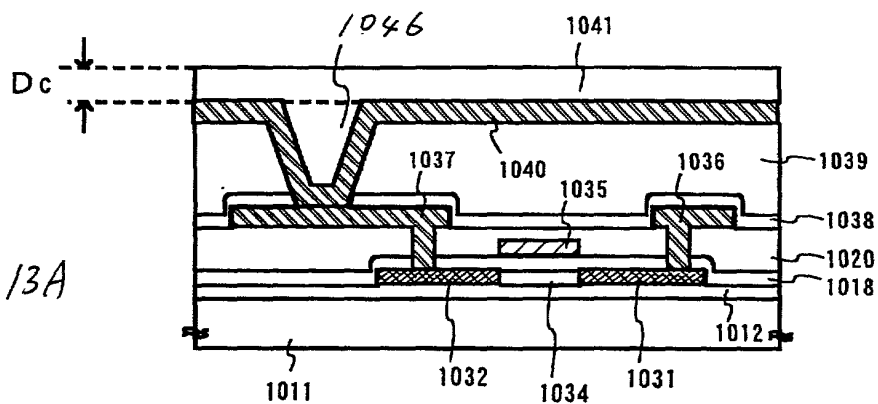


Fig. 13B

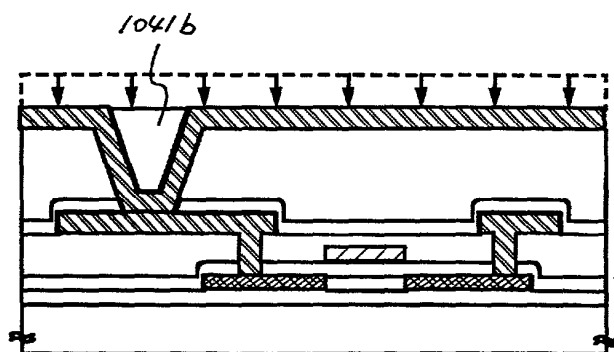


Fig. 13C

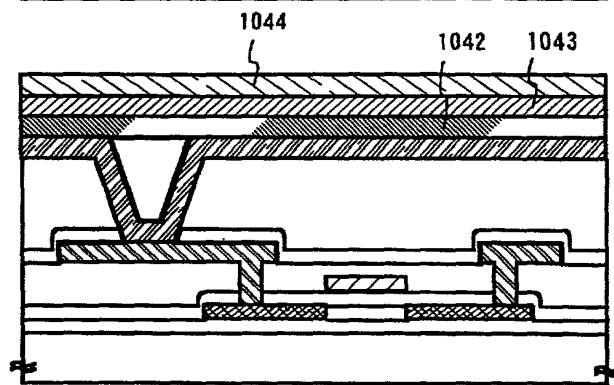
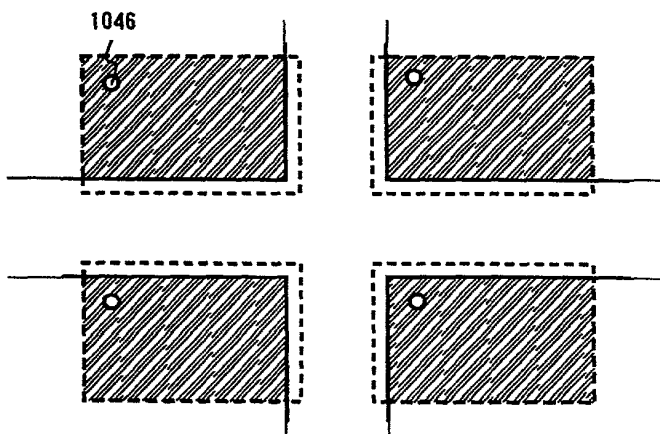


Fig. 13D



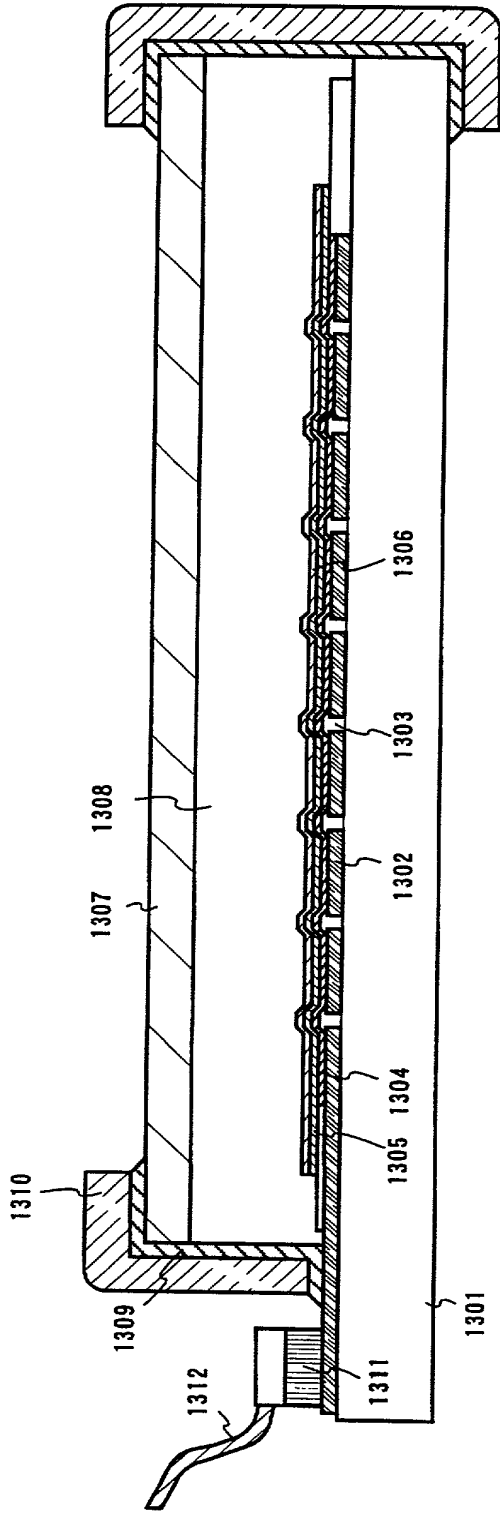


Fig. 14

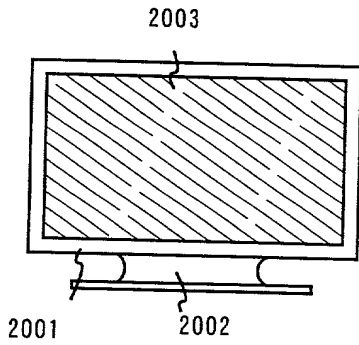


Fig. 15A

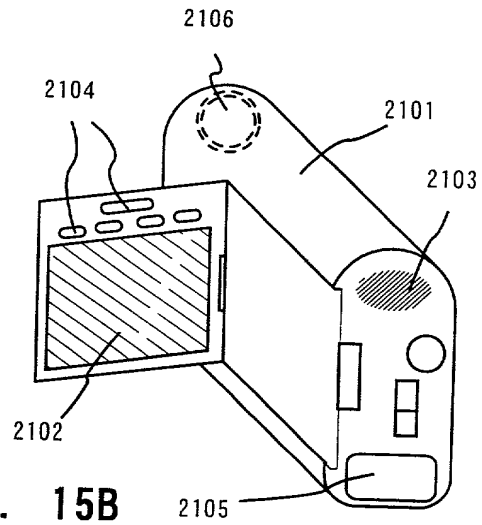


Fig. 15B

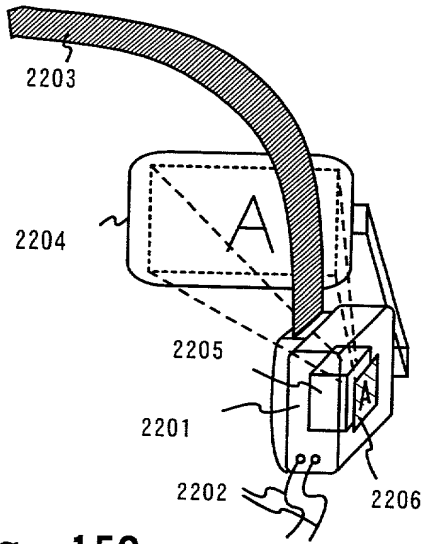


Fig. 15C

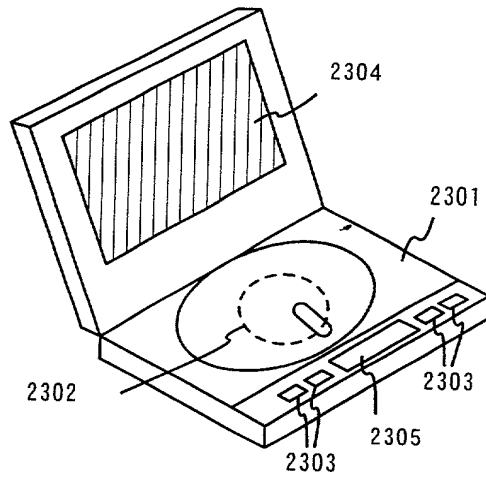


Fig. 15D

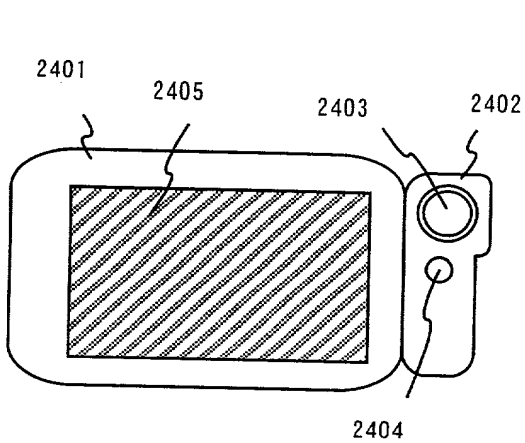


Fig. 15E

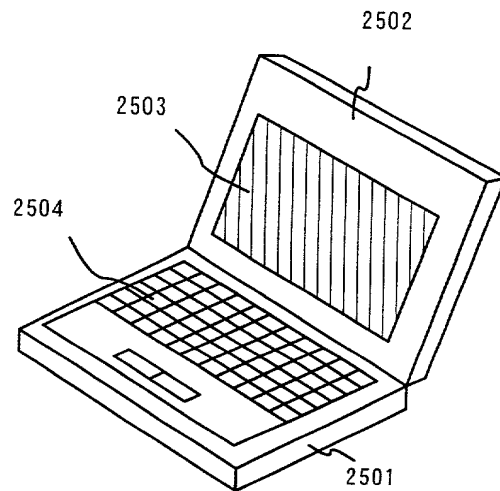


Fig. 15F

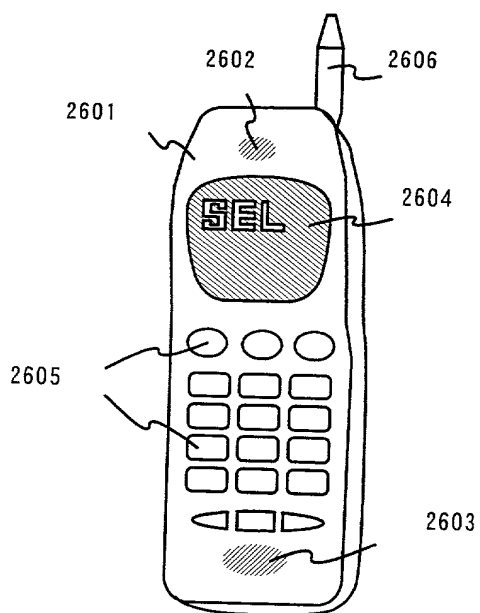


Fig. 16A

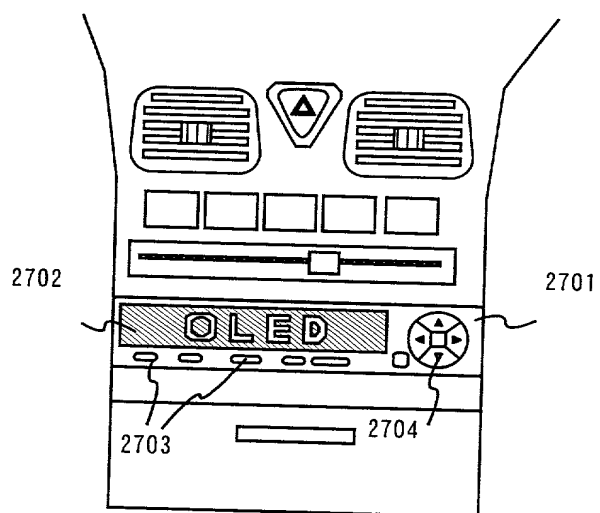


Fig. 16B